



2-27-04

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE RECD	ATTY.DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/644,603	08/20/2003	2811	1642	9319S-000540	11	23	11

27572

HARNESS, DICKEY & PIERCE, P.L.C.  
P.O. BOX 828  
BLOOMFIELD HILLS, MI 48303

CONFIRMATION NO. 5576

UPDATED FILING RECEIPT



\*OC000000011879930\*

Date Mailed: 02/11/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

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Takeshi Ashida, Matsumoto-shi, JAPAN;  
Masahiko Nakazawa, Nagano-ken, JAPAN;  
Masanori Yumoto, Matsunoto-shi, JAPAN;

## Domestic Priority data as claimed by applicant

## Foreign Applications

JAPAN 2002-240776 08/21/2002  
JAPAN 2003-197276 07/15/2003

If Required, Foreign Filing License Granted: 11/13/2003

Projected Publication Date: 05/20/2004

Non-Publication Request: No

Early Publication Request: No

## Title

Semiconductor device mounting method, semiconductor device mounting structure, *electro-* *electro-*

optical device, electro-optical device manufacturing method and electronic device

Preliminary Class

257

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Title 35, United States Code, Section 184  
Title 37, Code of Federal Regulations, 5.11 & 5.15**

**GRANTED**

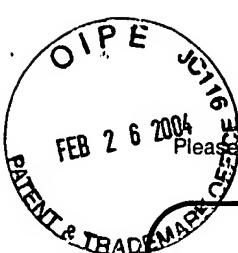
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HDP/SB/21 based on PTO/SB/21 (08-00)

## TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number	10/644,603
Filing Date	August 20, 2003
First Named Inventor	Kazuyuki Yamada
Group Art Unit	1642
Examiner Name	N/A

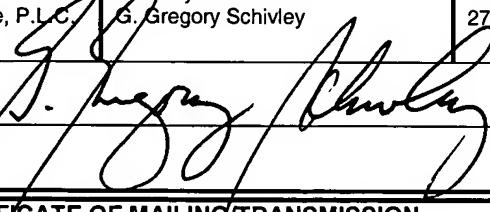
Attorney Docket Number

9319S-000540

### ENCLOSURES (check all that apply)

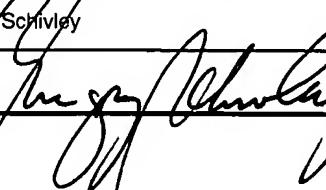
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Remarks		The Commissioner is hereby authorized to charge any additional fees that may be required under 37 CFR 1.16 or 1.17 to Deposit Account No. 08-0750. A duplicate copy of this sheet is enclosed.

### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Harness, Dickey & Pierce, P.L.C.	Attorney Name G. Gregory Schivley	Reg. No. 27,382
Signature			
Date	February 26, 2004		

### CERTIFICATE OF MAILING/TRANSMISSION

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Signature		Date	February 26, 2004

EV 406 074 603 US



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/644,603

Filing Date: August 20, 2003

Applicants: Kazuyuki Yamada, et al.

Group Art Unit: 1642

Examiner: N/A

Title: SEMICONDUCTOR DEVICE MOUNTING METHOD,  
SEMICONDUCTOR DEVICE MOUNTING STRUCTURE,  
ELECTRO-OPTICAL DEVICE, ELECTRO-OPTICAL DEVICE  
MANUFACTURING METHOD AND ELECTRONIC DEVICE

Attorney Docket: 9319S-000540

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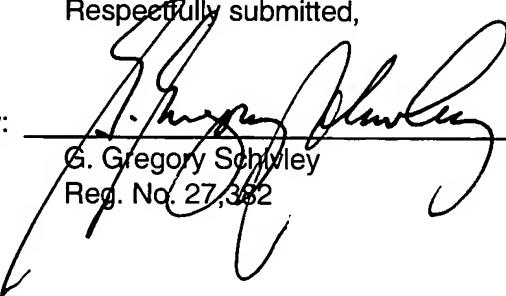
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REQUEST FOR CORRECTED OFFICIAL FILING RECEIPT

Sir:

We acknowledge receipt of the Updated Official Filing Receipt for the above-identified patent application. However, the title is incorrect. Please correct your records to show **SEMICONDUCTOR DEVICE MOUNTING METHOD, SEMICONDUCTOR DEVICE MOUNTING STRUCTURE, ELECTRO-OPTICAL DEVICE, ELECTRO-OPTICAL DEVICE MANUFACTURING METHOD AND ELECTRONIC DEVICE** as the correct title. This correction can be seen in red on the copy enclosed. Thank you for your assistance.

Respectfully submitted,

By: 

G. Gregory Schwley  
Reg. No. 27,382

Dated: February 26, 2004

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